

## Mr. Nadim Ahmed

Assistant Professor

B.E (ETCE), M.Tech (Microelectronics & VLSI)

### Overview:

Nadim Ahmed hails from Murshidabad district, West Bengal, India. He received the B.E. degree in Electronics & Telecommunication Engineering from Jadavpur University, Kolkata, India, in 2014, and the M.Tech. degree in Electrical Engineering (Specialization: Microelectronics & VLSI) from Indian Institute of Technology, Kanpur, India, in 2016. After completing his masters, he joined C-DOT: Centre for Development of Telematics, New Delhi (R&D Wing of Department of Telecommunication, Govt of India) as Research Engineer and worked there till September, 2019. Later, on 24<sup>th</sup> September of 2019, he joined Triveni Devi Bhalotia College, Raniganj as an Assistant Professor of Electronics after getting selected and recommended by WBCSC (West Bengal College Service Commission). Also, he is pursuing his PhD in engineering from IIT Kharagpur. The area of his research interest is semiconductor device modelling.

### Current Position:

Assistant Professor, Department of Electronics, Triveni Devi Bhalotia College, Raniganj

**Date of appointment to the present job:** 24<sup>th</sup> September, 2019

### Other Academic/Administrative post:

- ☐ Coordinator of Electronics Department, T.D.B College w.e.f. 1<sup>st</sup> July, 2020
- ☐ Member of Electronics Under-Graduate BOS (Board of Study), Kazi Nazrul University

### Academic background:

- ☐ **10<sup>th</sup> Exam: 2007**  
General Subjects, Narayanpur High School, Dakshin Dinajpur, Marks: 728/800 (91.0%)
- ☐ **12<sup>th</sup> Exam: 2009**  
Science, Khalatpur High Madrasah (H.S.), Howrah, Marks: 453/500 (90.6%)
- ☐ **B.E. (Bachelor of Engineering): 2010-2014**  
Electronics & Telecommunication Engineering, Jadavpur University, Kolkata, CGPA: 8.93/10
- ☐ **M.Tech. (Master of Technology): 2014-2016**  
Electrical Engineering (Specialization: Microelectronics & VLSI), IIT Kanpur, CGPA: 8.56/10
- ☐ **PhD (Engineering): 2020-Present**  
Electronics & Electrical Communication Engineering, IIT Kharagpur

### Academic Achievements:

1. Received **Topper 1%** certificate (**Gold+Elite**) with a score of 91% in NPTEL online certification course on "CMOS Digital VLSI Design" taught by Professor Sudeb Dasgupta, IIT Roorkee in 2021.
2. **UGC-CBSE** conducted **NET** (July, 2018) in Electronic Science
3. **GATE 2014:** AIR 241 (Score: 841/1000, Percentile: 99.88), **GATE 2017:** AIR 295 in ECE (Electronics & Communication Engineering) paper
4. **42<sup>nd</sup> Rank (General)** in **WBJEE 2010** (West Bengal Joint Entrance Examination) in Engineering
5. **WBMDFC** scholarship for securing higher marks in **12<sup>th</sup> Board Exam**
6. **MHRD** scholarship while pursuing M.Tech at **IIT Kanpur**

## Information about M.Phil/PhD etc.:

Pursuing PhD in Engineering under the department of Electronics & Electrical Communication Engineering, Indian Institute of Technology, Kharagpur

**PhD Topic:** Modeling and Simulation of p-GaN gate HEMTs (High Electron Mobility Transistors)

## Publications:

- ❑ Ahmed N., Dutta A. K., "Analytical models for the 2DEG concentration and gate leakage current in AlGaIn/GaN HEMTs", Solid-State Electronics, Vol. 132, pp. 64-72, March 2017.  
Weblink: <http://pdf-s3.xuebalib.com:1262/xuebalib.com.34756.pdf>
- ❑ Ghosh S., Chatterjee S., Ahmed N. and Misra I.S., "Interference Avoidance in Co-channel Deployment in Macro-femto Network Using New Approach of Solving Markov Chain", 2013 Annual IEEE India Conference (INDICON), Mumbai, 2013, pp. 1-5, doi: 10.1109/INDICON.2013.6725895.  
Weblink: <https://ieeexplore.ieee.org/document/6725895>

## Books and Chapters:

- ❑ Ahmed N., *History of Computers and Associated Technology (1940-2020)*. edited by Ajahar Islam et al. Science and Technology: History and Evolution. Kumud Publications, Delhi. July, 2020. ISBN 978-81-945060-3-4.

## Seminars, Webinars and Conferences attended:

- ❑ Ahmed N., "Interference Avoidance in Co-channel Deployment in Macro-femto Network Using New Approach of Solving Markov Chain." Participated and presented at IEEE conference, IIT Bombay, Mumbai, India. December 13-15, 2013.  
Web link: [https://drive.google.com/file/d/1OTxKj8yWf9uThG4vDxHanLgGdvdKORI\\_/view?usp=sharing](https://drive.google.com/file/d/1OTxKj8yWf9uThG4vDxHanLgGdvdKORI_/view?usp=sharing)
- ❑ Ahmed N., "Career Outreach E-Workshop." Participation of Webinar, Bidhan Chandra College, Asansol, WB, India. June 21, 2020.  
Web link: <https://drive.google.com/file/d/1ITyxkhVW56Pw0fOVDwMHxLtZ7n6MN69b/view?usp=sharing>
- ❑ Ahmed N., "Career Opportunities in Healthcare Industry." Participation of Webinar, organized by K J Somaiya College of Science & Commerce (Somaiya Vidya Vihar University), Mumbai, India. July 8, 2020.  
Web link: <https://drive.google.com/file/d/1jFmrTNrYJq4DnOxulG-3Oic8O85HqI1u/view?usp=sharing>
- ❑ Ahmed N., "5G and Beyond: how the world of telecommunication will look in next 5 years" Participation of Webinar, organized by Dept. of Computer Science, T.H.K Jain College, Kolkata, WB, India. July 11, 2020.  
Web link: <https://drive.google.com/file/d/1GcRI3wMEklzxs35YDYqvmrP9FAuabPt/view?usp=sharing>
- ❑ Ahmed N., "Algebra and Partial Differential Equations: Applications and Research Motivation" Participation of Webinar, Sir Gurudas Mahavidyalaya, Kolkata, WB, India. July 17, 2020.  
Web link: [https://drive.google.com/file/d/1ixqfJmwXCUwtdIh65Vhjj9J0N4tok\\_Hm/view?usp=sharing](https://drive.google.com/file/d/1ixqfJmwXCUwtdIh65Vhjj9J0N4tok_Hm/view?usp=sharing)

## Professional Course/Training:

- ❑ Summer Vocational Training, Advanced Telecom, BSNL-Kolkata, May 2013.  
Weblink: [https://drive.google.com/file/d/16\\_m4wq1EyiXOHQAXS\\_8jvOfhafXaubHk/view?usp=sharing](https://drive.google.com/file/d/16_m4wq1EyiXOHQAXS_8jvOfhafXaubHk/view?usp=sharing)

- ❑ Completed a computer course on “Programming in C” from NIIT Kolkata in 2014.

**Others/ Miscellaneous:**

Exams cleared for Jobs and Offers from different Govt and Private sectors:

- ❑ Selected for 'Associate Software Engineer' at Accenture Pvt. Service Limited in 2014 (Declined)
  - ❑ Cleared ISRO written test for Scientist/Engineer 'SC' post (Electronics) in 2014
  - ❑ Research Engineer, C-DOT, Govt of India in 2016 (Accepted)
  - ❑ Selected for JTO (Junior Telecom Officer), BSNL (A Govt of India Enterprise) in 2017 (Declined)
  - ❑ Assistant Professor in Electronics/Electronic Science, WBCSC, 2019 (Accepted)
  - ❑ Lecturer in Electronics & Telecommunication Engineering for West Bengal Government Polytechnic by WBPSC (got selected recently in 2021)
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